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LIST OF PRIOR ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Xinqiao Liu et al			
				FILING DATE 11/13/2001		GROUP 2612	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	A						
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		DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES NO
	I						
	J						
	K						
OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)							
<i>Ar</i>	L		A. Krymski et al.; "A high speed, 500 frames/s, 1024 X 1024 CMOS active pixel sensor;" Proceeding of the 1999 Symposium on VLSI Circuits, pp. 137-138, Jun. 1999				
<i>Ar</i>	M		N. stevanovic et al.; "A CMOS image sensor for high speed imaging;" Digest of Technical papers of the 2000 IEEE International Solid-State Circuits Conference, pp. 104-105, Feb. 2000				
<i>Ar</i>	N		R. L. Lagendijk et al.; "Maximum likelihood image and blur identification: a unifying approach;" Opt. Eng., Vol. 29, No. 5, pp. 422-435, May 1990				
EXAMINER <i>[Signature]</i> DATE CONSIDERED <i>1/4/2006</i>							
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U.S. PATENT DOCUMENTS


EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Ar	A	5 2 7 2 5 3 5	12/21/93	Elabd	358	213.11	6/13/91
Ar	B	5 4 6 1 4 2 5	10/24/95	Fowler et al.	348	294	2/15/94
Ar	C	5 5 8 3 3 6 7	12/10/96	Blossfeld	257	426	1/17/95
Ar	D	5 7 4 2 0 4 7	4/21/98	Buhler et al.	250	214	10/1/96
Ar	E	5 8 0 1 6 5 7	9/1/98	Fowler et al.	341	155	2/5/97
Ar	F	5 8 4 1 1 2 6	11/24/98	Fossum et al.	250	208.1	1/24/975
Ar	G	5 9 0 0 6 2 3	5/4/99	Tsang et al	250	208	8/11/97
Ar	H	5 9 6 9 7 5 8	10/19/99	Sauer et al.	348	241	6/2/97
Ar	AA	6 0 7 8 0 3 7	6/20/00	Booth, Jr.	250	208.1	4/16/98
Ar	BB	6 1 3 0 4 2 3	10/10/00	Brehmer et al.	250	208.1	7/10/98
Ar	CC	6 1 5 7 0 1 6	12/5/00	Clark et al.	250	208.1	9/30/97

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	ISSUE DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	I							
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Ar	L		Hon-Sum Wong; "Technology and device scaling considerations for CMOS imagers;" IEEE TRANSACTIONS ON ELECTRON DEVICE, VOL. 43, NO. 12, DECEMBER 1996					
Ar	M		D. Yang et al.; "Comparative analysis of SNR for image sensors with enhanced dynamic range;" SPIE, EL 1999					
Ar	N		E. R. Fossum; "CMOS image sensors: electronic camera-on-chip;" IEEE TRANSACTIONS ON ELECTRON DEVICE, VOL. 44, NO. 10, OCT. 1996					
Ar	O		S. Kleinfelder et al.; "A 10K frames/s 0.18μM CMOS digital pixel sensor with pixel-level memory;" DIGEST OF TECHNICAL PAPERS OF THE 2001 IEEE INTERNATIONAL SOLID-STATE CIRCUITS CONFERENCE, PP. 88-99. FEB. 2001					
Ar	P		D. Kundur et al.; "Blind image deconvolution;" IEEE SIGNAL PROCESSING MAGAZINE, VOL. 13 NO. 5, PP.43-64, MAY 1996					

Ar	Q	M. R. Banham; "Digital image restoration;" IEEE SIGNAL PROCESSING MAGAZINE, VOL. 14 NO. 2, PP.24-41, MARCH 1997
Ar	R	N. Stevanovic et al.; "A CMOS image sensor for high speed imaging;" ISSCC DIG. TECH. PAPERS, PP. 104-105, FEB 2000
Ar	S	S. Kleinfelder et al.; "A 10,000 frames/s 0.18 μ M CMOS digital pixel sensor with pixel-level memory;" ISSCC DIG. TECH. PAPERS, FEB 2001
Ar	T	O. Yadid-Pecht; "Wide intrascene dynamic range CMOS APS using dual sampling;" IEEE TRANS. ON ELECTRON DEVICES, VOL. 44 NO. 10, PP. 1721-1723, OCT. 1997
Ar	U	D. Yang; et al.; "A 640 X 512 CMOS image sensor with ultra-wide dynamic range floating-point pixel level ADC;" IEEE J. SOLID-STATE CIRCUITS, VOL. 34, NO. 12, PP.1821-1834, DEC. 1999
Ar	V	D. Yang et al.; "Comparative analysis of SNR for image sensors with enhanced dynamic range;" PROCEEDINGS OF THE SPIE, VOL. 3649, SAN JOSE, CA, JAN. 1999
Ar	W	A. El. Gamal et al.; "Pixel level processing why?, what?, and how?" PROCEEDINGS OF THE SPIE, VOL. 3650, PP. 2-13, JAN. 1999
Ar	X	S. H. Lim et al.; "Integration of image capture and processing-beyond single chip digital camera;" PROCEEDINGS OF THE SPIE, VOL. 4306, MARCH, 2001
Ar	Y	X. Liu et al.; "Photocurrent estimation from mutiple non-destructive samples in a CMOS image sensor;" PROC. OF SPIE, VOL. 4306, MARCH, 2001
Ar	Z	S. J. Decker; " A 256X256 CMOS imaging array with wide dynamic range pixels and column-parallel digital output;" IEEE JOURNAL OF SOLID STATE ICRCUITS, VOL. 33, PP. 2081-1091, DEC, 1998
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